



Form PTO-1449 (REV. 8-83)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. <b>42390.P6643</b>		SERIAL NO. <b>Not yet assigned</b>	
<b>INFORMATION DISCLOSURE CITATION</b> <i>(Use several sheets if necessary)</i>				APPLICANT <b>Brian D. Possley</b>			
				FILING DATE <b>March 4, 1999</b>		GROUP <b>Not yet assigned</b>	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
m		5,055,716	10-8-91	Abbas El Gamel	—	—	—
		5,068,548	11-26-91	Abbas El Gamel	—	—	—
		5,289,021	2-22-94	Abbas El Gamel	—	—	—
		5,341,041	8-23-94	Abbas El Gamel	—	—	—
		5,563,430	10-8-96	Hashimoto, et al.	—	—	—
		5,684,311	11-4-97	Ching-Hao Shaw	—	—	—
		5,780,883	7-14-98	Tran, et al.	—	—	—
m		5,789,966	8-4-98	Roland Albert Bechade	—	—	—
<b>FOREIGN PATENT DOCUMENTS</b>							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
m		SiArc Base Array, SPG Central Logic Engineering, Intel Corporation					
EXAMINER		DATE CONSIDERED					
m		12-27-99					
*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							